

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	7499	memory and redundant and chip	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/24 09:00
2	BRS	L2	4712	1 and @ay<1997	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/24 08:56
3	BRS	L3	212	2 and 257/\$.ccls.	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/24 08:57
4	BRS	L4	4	memory and (redundant adj chip) and @ay<1997 and 257/\$.ccls.	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/24 09:04
5	BRS	L5	3	memory and (spare adj chip) and @ay<1997 and 257/\$.ccls.	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/24 09:34
6	BRS	L6	46	memory and (spare adj chip) and @ay<1997	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/24 09:34

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	4	traffic adj control adj eeprom	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/24 08:23



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